



PATENT
8008-1052

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IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yasutaka NAKASHIBA

Conf. 2273

Application No. 10/812,282

Group 2815

Filed March 30, 2004

Examiner Jesse A. Fenty

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

RESPONSE

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 26, 2006

Sir:

This responds to the communication mailed December 19,
2006.

Remarks begin on page 2 of this paper.